EAST Search History

EAST Search History (Prior Art)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L24	127481	(mount$4 attach$6) same ("IC" chip semiconductor device) same (PCB circuit adj board)	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/01/05 09:49
L25	81	L24 and tolerance same (bondwir4 bond adj wir $4)	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/01/05 09:49
L26	14	L25 and (compensat$4 adjust$4 align$5) same ("IC" chip semiconductor) same (position location)	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/01/05 09:49
L27	220	(rotat$4 same (die chip) same (interposer via$hole bondpad lead$frame))	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/01/05 09:50
L28	64	L24 and L27	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/01/05 09:51
L29	12954	716/1,4-5,12-15,18.ccls. 703/13-14.ccls.	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/01/05 09:51
L30	0	L28 and L29	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/01/05 09:51
L31	52	L28 and @ad<="20041102"	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/01/05 09:51

EAST Search History (Interference)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp

L5	309	(Goto near Akihiro or Matsushima near Hironori or Ogawa near Hiroshige or Matsuda near Yoshio).inv.	US-PGPUB; USPAT; UPAD	OR	ON	2010/01/05 09:35
L6	861	((variable fluctuat$4 variation deviation) near10 (interposer ball$pad pad contact via$hole through$hole) near10 (position location)).clm.	US-PGPUB; USPAT; UPAD	OR	ON	2010/01/05 09:36
L7	919	((variable fluctuat$4 variation deviation) near10 (chip component) near10 (position location)).clm.	US-PGPUB; USPAT; UPAD	OR	ON	2010/01/05 09:36
L8	13545	716/1,4-5,12-15,18.ccls. 703/13-14.ccls.	US-PGPUB; USPAT; UPAD	OR	ON	2010/01/05 09:37
L9	19	L6 and L7	US-PGPUB; USPAT; UPAD	OR	ON	2010/01/05 09:38
L10	1	L5 and L9	US-PGPUB; USPAT; UPAD	OR	ON	2010/01/05 09:38
L11	1	L8 and L9	US-PGPUB; USPAT; UPAD	OR	ON	2010/01/05 09:38
L12	13	L9 and @ad<="20041102"	US-PGPUB; USPAT; UPAD	OR	ON	2010/01/05 09:38
L13	592	((off$set off adj set) with (chip die component) with (interposer ball$pad pad contact via$hole through $hole)).clm.	US-PGPUB; USPAT; UPAD	OR	ON	2010/01/05 09:41
L14	403	L13 and @ad<="20041102"	US-PGPUB; USPAT; UPAD	OR	ON	2010/01/05 09:41
L15	3	L8 and L14	US-PGPUB; USPAT; UPAD	OR	ON	2010/01/05 09:41
L16	0	L6 and L14	US-PGPUB; USPAT; UPAD	OR	ON	2010/01/05 09:42
L17	0	L7 and L14	US-PGPUB; USPAT; UPAD	OR	ON	2010/01/05 09:43
L18	50583	((variable fluctuat$4 variation deviation) with (position location)).clm.	US-PGPUB; USPAT; UPAD	OR	ON	2010/01/05 09:43
L19	6	L14 and L18	US-PGPUB; USPAT; UPAD	OR	ON	2010/01/05 09:43

L20	22	L14 and (variable fluctuat$4 variation deviation).clm.	US-PGPUB; USPAT; UPAD	OR	ON	2010/01/05 09:45
L21	316	((off$set off adj set) with (chip die) with (interposer ball$pad pad contact via$hole through $hole)).clm.	US-PGPUB; USPAT; UPAD	OR	ON	2010/01/05 09:46
L22	218	((off$set off adj set) with (chip die) with (interposer ball$pad pad via$hole)).clm.	US-PGPUB; USPAT; UPAD	OR	ON	2010/01/05 09:47
L23	4	((off$set off adj set) with (chip die) with (interposer ball$pad via$hole)).clm.	US-PGPUB; USPAT; UPAD	OR	ON	2010/01/05 09:47

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